

**This Page is Inserted by IFW Indexing and Scanning
Operations and is not part of the Official Record**

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- ☐ **BLACK BORDERS**
- ☐ **IMAGE CUT OFF AT TOP, BOTTOM OR SIDES**
- ☐ **FADED TEXT OR DRAWING**
- ☐ **BLURRED OR ILLEGIBLE TEXT OR DRAWING**
- ☐ **SKEWED/SLANTED IMAGES**
- ☐ **COLOR OR BLACK AND WHITE PHOTOGRAPHS**
- ☐ **GRAY SCALE DOCUMENTS**
- ☐ **LINES OR MARKS ON ORIGINAL DOCUMENT**
- ☐ **REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY**
- ☐ **OTHER:** _____

IMAGES ARE BEST AVAILABLE COPY.

As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.

L Number	Hits	Search Text	DB	Time stamp
-	323	717/140.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 14:55
-	76	717/142.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 14:56
-	205	717/143.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 14:56
-	137	717/156.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 15:52
-	849	716/2.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 14:58
-	4	716/2.ccls. and (description near3 file) and (data adj flow)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 14:58
-	4	716/2.ccls. and (syntax adj analysis)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 14:59
-	4	716/2.ccls. and (syntax adj analysis) and (description or circuit or model or high-level or (high adj level) or optimiz\$5 or boundary or (wait\$3 near2 time) or (function adj unit) or register or multiplexor or area or top-down or down-top or bottom-up or (top adj down) or (bottom adj up) or (highest adj level) or scheduling or priority or cost or control\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 15:08
-	7	716/2.ccls. and (description near3 file) and (data adj flow) and (description or circuit or model or high-level or (high adj level) or optimiz\$5 or boundary or (wait\$3 near2 time) or (function adj unit) or register or multiplexor or area or top-down or down-top or bottom-up or (top adj down) or (bottom adj up) or (highest adj level) or scheduling or priority or cost or control\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 15:21
-	3	("5859981" "5884066" "6367065" "2001/0034594").PN.	USPAT	2004/08/10 15:24
-	3469894	717/156.ccls. and (description near2 file) ad circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 15:52
-	0	717/156.ccls. and (description near2 file) and circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 15:56
-	0	717/156.ccls. and (description near2 file)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 15:57

-	16	717/156.ccls. and circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/10 15:57
-	10	5966534.URPN.	USPAT	2004/08/10 16:02
-	23	("5021947" "5381550" "5465368" "5646544" "5737631" "5828858" "5892961" "5907580" "5933642" "5959811" "5966534" "5970254" "5999734" "6016395" "6023742" "6078736" "6094065" "6088043" "6120551" "6150838" "6230307" "6237029" "6507947" "2002/0042907").PN.	USPAT	2004/08/10 16:15
-	14	("5021947" "5381550" "5465368" "5646544" "5737631" "5828858" "5892961" "5907580" "5933642" "5959811" "5966534" "5970254" "5999734" "6016395" "6023742" "6078736" "6094065" "6088043" "6120551" "6150838" "6230307" "6237029" "6507947" "2002/0042907").PN. and (compil\$5)	USPAT	2004/08/10 16:14
-	23	("5021947" "5381550" "5465368" "5646544" "5737631" "5828858" "5892961" "5907580" "5933642" "5959811" "5966534" "5970254" "5999734" "6016395" "6023742" "6078736" "6094065" "6088043" "6120551" "6150838" "6230307" "6237029" "6507947" "2002/0042907").PN. and ((description near3 file) or (data adj flow) or description or circuit or model or high-level or (high adj level) or optimiz\$5 or boundary or (wait\$3 near2 time) or (function adj unit) or register or multiplexor or area or top-down or down-top or bottom-up or (top adj down) or (bottom adj up) or (highest adj level) or scheduling or priority or cost or control\$4)	USPAT	2004/08/10 16:16
-	1111	download\$3 near3 firmware	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:46
-	9	download\$3 near3 firmware same (driver and function)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:46



US Patent & Trademark Office

[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)

 Search: ☒ The ACM Digital Library ☐ The Guide


[Feedback](#) [Report a problem](#) [Satisfaction survey](#)

Java driven codesign and prototyping of networked embedded systems

Full text Pdf (69 KB)

Source [Annual ACM IEEE Design Automation Conference archive](#)
Proceedings of the 36th ACM/IEEE conference on Design automation [table of contents](#)
 New Orleans, Louisiana, United States
 Pages: 794 - 797
 Year of Publication: 1999
 ISBN:1-58133-109-7

Authors [Josef Fleischmann](#)
[Klaus Buchenrieder](#)
[Rainer Kress](#)

Sponsors EDAC : Electronic Design Automation Consortium
 IEEE-CAS : Circuits & Systems
 SIGDA: ACM Special Interest Group on Design Automation

Publisher ACM Press New York, NY, USA

Additional Information: [references](#) [citations](#) [index terms](#) [collaborative colleagues](#)

Tools and Actions: [Discussions](#) [Find similar Articles](#) [Review this Article](#)
[Save this Article to a Binder](#) [Display in BibTex Format](#)

DOI Bookmark: Use this link to bookmark this Article: <http://doi.acm.org/10.1145/309847.310068>
[What is a DOI?](#)

↑ REFERENCES

Note: OCR errors may be found in this Reference List extracted from the full text article. ACM has opted to expose the complete List rather than only correct and linked references.

- 1 [P. Bellows , B. Hutchings, JHDL - An HDL for Reconfigurable Systems, Proceedings of the IEEE Symposium on FPGAs for Custom Computing Machines, p.175, April 15-17, 1998](#)
- 2 Peter Clarke: Tricore to get flash FPGA integration. In EE Times, No. 1000; CMP Media, 1998.
- 3 [Josef Fleischmann , Klaus Buchenrieder , Rainer Kress, A hardware/software prototyping environment for dynamically reconfigurable embedded systems, Proceedings of the 6th international workshop on Hardware/software codesign, p.105-109, March 15-18, 1998, Seattle, Washington, United States](#)
- 4 [Rachid Helaihel , Kunle Olukotun, Java as a specification language for hardware-software systems, Proceedings of the 1997 IEEE/ACM international conference on Computer-aided design, p.690-697, November 09-13, 1997, San Jose, California, United States](#)
- 5 JavaBeans API specification, Sun Microsystems, <http://java.sun.com/beans>, 1998.

- 6 Asawaree Kalavade , Pratyush Moghé, A tool for performance estimation of networked embedded end-systems, Proceedings of the 35th annual conference on Design automation, p.257-262, June 15-19, 1998, San Francisco, California, United States
- 7 T. Kuhn, W. Rosenstiel: Java Based Modeling and Simulation of Digital Systems on Register Transfer Level. In Workshop on System Design Automation, 1998.
- 8 Eric Lechner , Steven A. Guccione, The Java environment for reconfigurable computing, Proceedings of the 7th International Workshop on Field-Programmable Logic and Applications, p.284-293, September 01-03, 1997
- 9 National Semiconductor: Napal000 Adaptive Processor, [http://www, national.com/appinfo/milaero/napal000](http://www.national.com/appinfo/milaero/napal000), 1998.
- 10 Stuart Nisbet , Steve Guccione, The XC6200DS development system, Proceedings of the 7th International Workshop on Field-Programmable Logic and Applications, p.61-68, September 01-03, 1997
- 11 R. Passerone, et al.: Modeling Reactive Systems in Java. In Int. High Level Design Validation and Test Workshop, Nov. 1997.
- 12 M. Vasilko: Dynamically Reconfigurable Hardware WWW Library, Bournemouth University, [http://dec.bournemouth 'ac'uk/drhwl-lib/](http://dec.bournemouth.ac.uk/drhwl-lib/)
- 13 James Shin Young , Josh MacDonald , Michael Shilman , Abdallah Tabbara , Paul Hilfinger , A. Richard Newton, Design and specification of embedded systems in Java using successive, formal refinement, Proceedings of the 35th annual conference on Design automation conference, p.70-75, June 15-19, 1998, San Francisco, California, United States

↑ CITINGS

Roman Lysecky , Frank Vahid, Prefetching for improved bus wrapper performance in cores, ACM Transactions on Design Automation of Electronic Systems (TODAES), v.7 n.1, p.58-90, January 2002

↑ INDEX TERMS

Primary Classification:

B. Hardware

↳ B.4 INPUT/OUTPUT AND DATA COMMUNICATIONS

Additional Classification:

D. Software

↳ D.4 OPERATING SYSTEMS

↳ D.4.7 Organization and Design

↳ **Subjects:** Real-time systems and embedded systems

J. Computer Applications

General Terms:

Design, Management, Reliability

↑ Collaborative Colleagues:

<u>Klaus Buchenrieder:</u>	<u>Siemens Ag</u>	<u>Jerzy W. Rozenblit</u>
	<u>Josef Fleischmann</u>	<u>Stephan Schulz</u>
	<u>Germany</u>	<u>Alexander Sedlmeier</u>
	<u>Stefan Kahlert</u>	<u>Ch. Veith</u>
	<u>Rainer Kress</u>	<u>Christian Veith</u>
	<u>Dieter Monjau</u>	<u>Wayne Wolf</u>
	<u>Michael Mrva</u>	
	<u>Munich</u>	
	<u>Ulrich Nageldinger</u>	
	<u>Andreas Pyttel</u>	
<u>Josef Fleischmann:</u>	<u>Klaus Buchenrieder</u>	
	<u>Norbert Fröhlich</u>	
	<u>Norbert Froehlich</u>	
	<u>Volker Glöckel</u>	
	<u>Rainer Kress</u>	
	<u>Martin Peller</u>	
	<u>Rolf Schlagenhaft</u>	
	<u>Philip A. Wilsey</u>	
<u>Rainer Kress:</u>	<u>Andreas Ast</u>	<u>Peter Monk</u>
	<u>Jürgen Becker</u>	<u>Ulrich Nageldinger</u>
	<u>Jrgen Becker</u>	<u>Andreas Pyttel</u>
	<u>Juergen Becker</u>	<u>Helmut Reinig</u>
	<u>Klaus Buchenrieder</u>	<u>William Rundell</u>
	<u>David Colton</u>	<u>Karin Schmidt</u>
	<u>Josef Fleischmann</u>	<u>Alexander Sedlmeier</u>
	<u>Reiner W. Hartenstein</u>	<u>Ian H. Sloan</u>
	<u>Michael Herz</u>	<u>Axel Zinn</u>
	<u>Kuo-Ming Lee</u>	

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2004 ACM, Inc.
[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)  [Real Player](#)



US Patent & Trademark Office

[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)Search: ☒ The ACM Digital Library ☐ The Guide

embedded functions and driver

SEARCH

[Feedback](#) [Report a problem](#) [Satisfaction survey](#)Terms used **embedded functions** and **driver**Found **22,245** of **140,980**Sort results
by

relevance

Display
results

expanded form

[Save results to a Binder](#)[Search Tips](#)☐ Open results in a new
window[Try an Advanced Search](#)[Try this search in The ACM Guide](#)

Results 1 - 20 of 200

Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [next](#)

Best 200 shown

Relevance scale ☐ ☐ ☐ ☐ ☐**1 Lambdas in the liftshaft—functional programming and an embedded architecture**

Malcolm Wallace, Colin Runciman

October 1995 **Proceedings of the seventh international conference on Functional programming languages and computer architecture**


Full text available: pdf(810.30 KB)

Additional Information: [full citation](#), [references](#), [index terms](#)

18 Automatic generation and targeting of application specific operating systems and embedded systems software

L. Guthier, S. Yoo, A. Jerraya

March 2001 **Proceedings of the conference on Design, automation and test in Europe**

Full text available:  pdf(91.74 KB)


Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



7 Embedded application design using a real-time OS

David Stepner, Nagarajan Rajan, David Hui

June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation**

Full text available:  pdf(105.02 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

2 A bitstream reconfigurable FPGA implementation of the WSAT algorithm

Leong, P.H.W.; Sham, C.W.; Wong, W.C.; Wong, H.Y.; Yuen, W.S.; Leong, M.P.;

Very Large Scale Integration (VLSI) Systems, IEEE Transactions on , Volume:

9 , Issue: 1 , Feb. 2001

Pages:197 - 201